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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10017389	12 18 2001	257		2811	
**APPLICANTS: Ishibashi Akihiko, Tsujimura Ayumu, Kawaguchi Yasutoshi, Otsuka Nobuyuki, Ohnaka Kiyoshi					
**CONTINUING DATA VERIFIED: <i>yes</i> <i>OK</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>yes</i> <i>OK</i> JAPAN 2000-386624 12/20/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 condition met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledge Examiner's initials				ATTORNEY DOCKET NO 740819-720	
TITLE : Method for fabricating semiconductor, method for fabricating semiconductor substrate, and semiconductor light emitting device					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE Amount Due Date Paid		DRAWING Sheets Drwg. Figs. Drwg. Print Fig.	
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